

**INSTALLATION OF INTELLIGENT SENSORS FOR THE HIGH-SPEED
ACQUISITION OF DATA VIA THE ETHERNET NETWORK**

This present invention concerns the technical area of the
5 high-speed transmission of acquisition data over an Ethernet
network.

More precisely, the subject of the invention concerns one
and preferably several sensors of the intelligent type, each
incorporated into the Ethernet network as a single node.

10 In many applications, the need arises to remotely
transmit measurement data acquired by one or more sensors
located at distant sites. Thus, we are familiar, for example,
from document US2003-0036875, with a computer network for
communication between several computers. At least one of the
15 computers, known as the master, is connected by a bus to a
series of sensors for the measurement of diverse physical
magnitudes. Such an architecture is used to remotely transmit
configuration data from the measurement sensors, via a special
communication bus, between the computer and the measurement
20 sensors. This document does not describe a technique for
acquisition of the high-speed data, which generally involves a
high implementation cost, a relatively fixed measurement
architecture that is difficult to develop, and a slowness in
the transmission of the acquired data.

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we are also familiar with the use of the Ethernet network
for the acquisition of data, such as measurement data coming

from sensors. However, the Ethernet network does not allow the operation of the different sensors to be correlated.

The subject of this present invention therefore aims to remedy the drawbacks mentioned above, by proposing an
 5 installation that is simple to implement, allowing the high-speed acquisition of acquisition data, effected by at least one detector. Another objective of the invention is to propose an installation that provides simplified access to the detector, as well as a simplified and correlated distribution
 10 of the acquisition data.

In order to attain such an objective, the subject of the invention concerns an installation for the high-speed acquisition of acquisition data over the Ethernet network, with several nodes, where at least one of the nodes of the
 15 Ethernet network constitutes a client/server detection unit with at least one detector delivering acquisition data, characterised in that each detection unit includes:

- self-triggering resources for reading the acquisition data so that the said detection unit is able to operate
 20 independently,
- reading and processing resources that are independent of the other nodes,
- resources for transmission of the acquisition data via the network to at least one other node.
- 25 - and a clock unit allowing correlation between the clocks of the 20 detection units, where each clock unit includes:
 - resources for receiving a clock synchronisation signal, generated by one of the said units and having encoded instructions,
 - 30 • resources for transmission of an acknowledge signal to the clock unit transmitting the synchronisation signal,
 - and resources for processing the encoded instructions, in particular to increment an event marking sensor.

According to one advantageous implementation characteristic, at least one of the nodes of the Ethernet network constitutes a client/server user unit designed to provide the detection unit with configuration data from the said unit and to receive acquisition data transmitted by the detection unit.

Advantageously, each detection unit includes:

- a detector performing the conversion of a physical magnitude into electrical signals delivered on several output paths,
- 10 - a sequencer with resources performing :
 - sequencing for reading the acquisition data from the detector and of the configuration data,
 - storage of the acquisition and configuration data,
 - analysis and processing of the acquisition data from the detector,
 - 15 • the interface to a network processor,
- and an Ethernet network processor with resources providing:
 - the interface to the sequencer,
 - reception of the data sent by the user unit to perform configuration of the detector and of the sequencer,
 - 20 • processing of the acquisition data,
 - and transmission of the acquisition data from the detector to the user unit.

Advantageously, the sequencer includes resources performing:

- 25 - formatting of the acquisition data from the detector and of the information resulting from the processing effected by the detection unit,
- storage in memory of the processed and formatted acquisition data, and
- 30 - temporal marking of the trigger for acquisition of the data.

According to a preferred implementation example, the sequencer is based upon an FPGA (field-programmable gate array device).

According to one implementation characteristic, the Ethernet network processor includes resources performing:

- retrieval of the data stored in the memory by the sequencer,
- analysis and processing of the said data,
- formatting of the processed data,
- and shared management of the data processing with other nodes of the network.

For example, the sequencer performs the storage of the data in a memory inside or outside the sequencer.

According to a preferred implementation example, the detector includes

- a sensitive sensor with a series of output paths,
- a sub-module for reading the acquisition data, controlled by the sequencer and including a frontal electronic unit,
- and a control sub-module, managed by the sequencer, to configure and control the frontal electronic unit.

According to this implementation example, the frontal electronic unit includes:

- resources for reading the acquisition data, resources for selection of the acquisition mode and resources for selection of the acquisition trigger source,
- resources for amplification and shaping of the signals
- and resources for receiving the configuration parameters.

Preferably, the control sub-module includes resources to control the frontal electronic unit and to control the detector.

Various other characteristics come out of the description that follows, with reference to the appended drawings which show, by way of non-limiting examples, 15 forms of implementation of the subject of the invention.

Figure 1 is a schematic view of one implementation example of an installation according to the invention.

Figure 2 is a functional diagram of a detection unit according to the invention.

5 As demonstrated more precisely in **figure 1**, the subject of the invention concerns an installation **1** employing an Ethernet network **2** with several nodes **N**. According to one characteristic of the invention, at least one of the nodes **N** of the Ethernet network constitutes a client/server detection
10 unit **3** with at least one detector **4** delivering acquisition data.

A detector **4** is designed to measure a physical magnitude in the general sense. In a preferred implementation example, the detector **4** is able to capture particles and to create a
15 quantity of charge corresponding to the particle/sensitive-cell interaction. The detector **4** thus performs the conversion of a physical magnitude into electrical signals. According to a preferred implementation example, the detector **4** delivers electrical signals on several output paths, as will be
20 explained in the remainder of the description.

Each detection unit **3** also includes an acquisition controller **6** forming the intelligent acquisition electronic part. This acquisition controller **6** receives the electrical signals from the detector **4** and has its own IP (Internet
25 Protocol) address so that the detector **4** is connected directly to the Ethernet network via the intelligent acquisition controller **6**.

According to one characteristic of the invention, the detection unit **3** includes self-triggering resources for
30 reading the acquisition data delivered by the detector **4**, so that the detection unit **3** is able to operate independently. According to another characteristic of the invention, the acquisition controller **6** includes resources for reading and

processing the data, independent of the other nodes **N** of the Ethernet network. In other words, the acquisition controller **6** is designed to perform processing in an independent manner.

In addition, the acquisition controller **6** includes
5 resources for transmission of the acquisition data via the Ethernet network **2** to at least one other node **N**. It should be noted that the acquisition controller **6** provides all of the features necessary for high-speed, multipath acquisition (pre-processing and formatting of the data, temporal marking, and
10 transmission on the network), for operational control of the detector **4** and for processing of the data.

This acquisition controller **6** also allows the synchronisation of a detector **4** either with other detectors, or with an external clock, for the construction of a complete
15 synchronised and distributed acquisition system, as will be described in greater detail in the remainder of the description.

The acquisition controller **6** is controlled via the Ethernet network, by a client/server user unit **10** constituting
20 a node **N** of the Ethernet network **2**. This user unit **10** is designed to provide the detection unit **3** with configuration data from the said unit, and to receive the acquisition data transmitted by the detection unit **3**.

As demonstrated more precisely in figure **2**, this
25 acquisition controller **6** includes a sequencer **13** communicating with the detector **4**, and an Ethernet network processor **14** connected to the Ethernet network **2** and to the sequencer **13**. The sequencer **13** is connected to the detector **4** by at least one analogue/digital converter **15₁**, and by at least one
30 digital/analogue converter **15₂**.

According to the invention, the sequencer **13** includes resources performing the sequencing for reading the acquisition data from the detector **4**, and of the configuration

data transmitted by the user unit 10. The sequencer 13 includes resources that format the acquisition data from the detector 4 and the information resulting from the processing effected by the detection unit 3. The sequencer 13 also includes resources that store the processed and formatted acquisition data, as well as the configuration data. The storage of these data is effected in an internal memory 16 or, as illustrated, external to the sequencer 13 and connected to the Ethernet network processor 14.

10 The sequencer 13 also includes resources forming the interface to the Ethernet network processor 14. The Ethernet network processor 14 includes resources forming the interface to the sequencer 13 and resources providing for retrieval of the data stored in the memory 16 by the sequencer 13.

15 Advantageously, the Ethernet network processor 14 includes resources performing the analysis and processing of the retrieved data and formatting of the said data. The Ethernet network processor 14 also includes resources allowing the transmission of the acquisition data from the detector 4 to the user unit 10. The Ethernet network processor 14 also includes resources for receiving the data sent by the user unit 10, with a view to configuring the detector 4 and the sequencer 13.

25 The description that follows illustrates one implementation example of a client/server detection unit 3. According to this implementation example, the detector 4 is a multi-way sensor that is sensitive to photons. Such a detector 4 can be a multi-pixel photon detector, a multi-anode photo multiplier (maPMT), hybrid photodiodes (HPD) or avalanche photodiodes (APD).

30 The detector 4 also includes:

- a sub-module for reading the acquisition data, controlled by the sequencer 13 and including a frontal electronic unit,

- and a control sub-module managed by the sequencer **13** to configure and control the frontal electronic unit.

The frontal electronic unit includes:

- resources for amplification and shaping of the signals delivered by the detector **4** (a slow path for precision measurements and a fast path for triggering),
- and resources for receiving the configuration parameters from the detector **4** (such as the polarisation signals or the configuration and read registers).

The frontal electronic unit also includes:

- resources for reading the multiplexed acquisition data of the slow path and analogue/digital conversion resources,
- resources for selection of the acquisition mode, namely calibration, test, single-channel acquisition or multi-channel acquisition,
- and resources for selection of the acquisition trigger source (external triggering or self-triggering).

The control sub-module, managed by the sequencer **13**, is used to control:

- the frontal electronic unit in performing, for example, adjustment of the polarisation voltages and currents, the control of the triggering levels for each path, and control of the calibration signals,
- and the detector **4**, for control of the polarisation and supply voltages.

The sequencer **13** includes resources that do:

- the read sequencing, allowing the reception and generation of the clock signals, generation of the read configuration registers, management of the digital inputs/outputs and management of the converters **15₁**, **15₂**,
- the data processing, such as removal of the zeros, the validation of events by external triggers,
- formatting of the data,

- and analysis of the data, for example, by the rapid calculation of the magnitudes associated with the data and pre-selection of the events.

According to a preferred implementation variant, the
5 sequencer is built around an FPGA device.

According to the invention, the installation 1 allows distributed multiple acquisition via the Ethernet network 2. In this regard, each detection unit 3 includes a clock unit 21 allowing correlation between the clocks of the detection units
10 3 and performing temporal marking. One of the detection units 3 or the user unit 10, called the master, generates a clock synchronisation signal with encoded instructions. Each clock unit includes resources for receiving the clock synchronisation signal and resources for sending an
15 acknowledge signal to the clock unit transmitting the synchronisation signal. Each clock unit also includes resources for processing the encoded instructions, in particular to increment an associated event-marking sensor. Such an incrementation is used to synchronise all the clocks
20 of the different nodes of the distributed acquisition system.

The above description demonstrates that the subject of the invention allows the acquisition of measurement data exclusively via the Ethernet network, to the extent that no intermediate bus N is employed. Each of the nodes of the
25 Ethernet network in the form of a detection unit 10 is controlled and read by an Ethernet chip installed in each detection unit visible transparently by the network.

In addition, each client/server user unit 10 controls, the detection units 3 via a direct Ethernet connection,
30 performing, for example, configuration of the frontal electronic unit, control of the sequencing and the digitisation, as well as the processing and the transmission of the data over the Ethernet network.

Finally, each detection unit 3 includes an intelligent acquisition controller 6 with its own IP address, while still being independent in order to perform slow control, processing and reading of the acquisition data.

5 The architecture proposed by the invention is low in cost and is standardised by its association with the Ethernet standard, is modular terms of the design of the data acquisition scheme, and is flexible in terms of the data processing.

10 The invention is not limited to the examples described and illustrated, since various changes can be made to it without moving outside of its claimed coverage.